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	Application Number		10726902	
	Filing Date		2003-12-03	
INFORMATION DISCLOSURE	First Named Inventor Mitchell Alsup		nell Alsup	1
STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Art Unit		2183	
(Not for submission under 37 Orix 1.53)	Examiner Name		Robert E. Pennema	
	Attorney Docket Number		5500-88700	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99) Application Number 10726902 Filing Date 2003-12-03 First Named Inventor Mitchell Alsup Art Unit 2183 Examiner Name Robert E. Pennema Attorney Docket Number 5500-88700

RF 1 Examination Report for UK 0611775.8, mailed November 30, 2006, 5 pages.							
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Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, (page(s), volume-issue number(s), publisher, city and/or country where published.			
Cl	Lai, et al., "Dead-Block Prediction & Dead-Block Correlating Prefetchers," 2001 IEEE, 11 pages.			
C2	Slechta, et al., "Dynamic Optimization of Micro-Operations," 2002 IEEE, 12 pages.			
C3	Jacobson, "High-Performance Frontends for Trace Processors," University of Wisconson-Madison, 1999, pp. 19, 20, 36, 37, 104-122.			
	U.S. Application Serial No. 10/822,468, filed 4/12/04, Alsup, et al.			
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	No. ¹ C1 C2	No. Intended that of the author (in Carta Serial, Symposium, catalog, etc.), date, (page(s), volume-issue number(s), publisher, city and/or country where published. C1 Lai, et al., "Dead-Block Prediction & Dead-Block Correlating Prefetchers," 2001 IEEE, 11 pages. C2 Slechta, et al., "Dynamic Optimization of Micro-Operations," 2002 IEEE, 12 pages. C3 Jacobson, "High-Performance Frontends for Trace Processors," University of Wisconson-Madison, 1999, pp. 19, 20, 36, 37, 104-122.		

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